



- 📁 Drafts
- 📁 Pending
- 📁 Active
 - 🔍 L1: (2) ("5319591").PN.
 - 🔍 L2: (2) ("4045781").PN.
 - 🔍 L3: (4661) (711/5,100,104,105,170).CCLS.
 - 🔍 L4: (24639) memory adj module
 - 🔍 L5: (615184) module
 - 🔍 L7: (1598) 3 and 5
 - 🔍 L6: (736) 3 and 4
 - 🔍 L8: (287107) impedance
 - 🔍 L9: (1869218) voltage
 - 🔍 L10: (62) 6 and 8 and 9
- 📁 Failed
- 📁 Saved
- 📁 Favorites
- 📁 Tagged (1)
- 📁 UDC
- 📁 Queue
- 📁 Trash

	U	1	Document ID	Issue Date	Pages	Inventor	Title	Current OR	Cu
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5802395 A	19980901	9	Connolly; Brian J. et al.	High density memory modules with improved data bus performance	710/14	365 710